

This listing of claims will replace all prior versions and listings of claims in the application:

Listing of Claims:

Claims 10-18 (cancelled).

Claim 19 (new): A rake receiver for telecommunication systems with wireless telecommunication between transceivers, the rake receiver comprising:

a pipeline architecture including three pipeline stages in which individual signal processing steps are processed on a pipeline;

registers operatively coupled to the pipeline stages for buffering data between the pipeline stages;

a memory device operatively coupled to a first of the pipeline stages, wherein the first pipeline stage reads received chips from the memory device and calculates an address by adding a value from a free running addresses counter to at least one rake-finger dependant offset;

an interpolator operatively coupled to a second of the pipeline stages to determine conjugate complex coefficients by interpolating between two channel estimations; and

an accumulator operatively coupled to a third of the pipeline stages to provide early/late tracking of rake fingers.

Claim 20 (new): The rake receiver as claimed in claim 1, further including a code combining/soft-handover-circuit which supports a soft handover in the first pipeline stage.

Claim 21 (new): The rack receiver as claimed in claim 1, further including a code combining/soft-handover-circuit which provides code combining in the first pipeline stage.

Claim 22 (new): The rake receiver as claimed in claim 19, wherein the pipeline architecture is flexibly adapted to word widths and clock rates by inserting parallel processing branches.

Claim 23 (new): The rake receiver as claimed in claim 20, wherein the pipeline architecture is flexibly adapted to word widths and clock rates by inserting parallel processing branches.